

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Method of Manufacturing a Semiconductor Device and a Method for Fixing the Semiconductor Device Using Substrate Jig							
Application Number :								
Date :								
First Named Applicant:	Kazuo TESHIROGI							
Confirmation Number:								
Attorney Docket Number:	020334A							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>William G. Kratz Jr. Registered Number: 22,631</td><td>/WGK/</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	William G. Kratz Jr. Registered Number: 22,631	/WGK/	Attorney
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William G. Kratz Jr. Registered Number: 22,631	/WGK/	Attorney						

Documents being submitted:	Files
us-ids	u020334A-usidst.xml us-ids.dtd us-ids.xsl
us-request	u020334A-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	u020334A-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	spec2-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd figpg1.tif figpg2.tif figpg3.tif figpg4.tif figpg5.tif figpg6.tif figpg7.tif figpg8.tif figpg9.tif figpg10.tif figpg11.tif figpg12.tif

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us-declaration	decpg1A.tif
us-declaration	decpg2.tif
us-declaration	decpg3.tif
us-declaration	decpg4.tif
us-declaration	decpg5.tif
us-declaration	decpg6.tif
Comments	